CLAIM AMENDMENTS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously Presented) A method comprising:

identifying a rate of change of a number of pending instructions stored in an instruction buffer; and

adjusting a system characteristic based on the rate of change of the number of pending instructions, wherein a power consumption of a system is modified based on the system characteristic.

2. – 7. (Canceled)

8. (Previously Presented) The method as in Claim 1, wherein adjusting the system characteristic includes altering the number of bits used to represent a multimedia data processed by the system.

9. (Original) The method as in Claim 8, wherein the multimedia data includes video data.

10. (Original) The method as in Claim 8, wherein the multimedia data includes audio data.

11. (Currently Amended) A method comprising:

identifying an operating characteristic of an instruction buffer, the operating characteristic comprising at least one of a buffer fullness[[,]] or a rate of change of a number of pending instructions stored in the instruction buffer~~; or a type of instructions stored in the instruction buffer;~~ and

adjusting a system characteristic based on the operating characteristic, wherein a power consumption of a system is modified based on the system characteristic and wherein adjusting the system characteristic includes modifying a clock speed.

12. (Previously Presented) The method as in Claim 11, wherein a nominal power provided to the system is modified based on an amount of power needed for the clock speed used.

13. (Previously Presented) The method as in Claim 11, wherein a number of bits used to represent a multimedia data processed by the system is reduced.

14. – 15. (Canceled)

16. (Previously Presented) The method as in Claim 1, wherein adjusting the system characteristic includes modifying a nominal power provided to the system.

17. (Previously Presented) The method as in Claim 16, wherein a clock speed is modified based on the modification of the nominal power.

18. (Previously Presented) The method as in Claim 16, wherein a number of bits used to represent multimedia data is modified based on the modification of the nominal power.

19. – 27. (Canceled)

28. (Currently Amended) A system comprising:

an instruction buffer to store pending instructions;

a threshold register to store a statistic threshold;

a[[n]] buffer monitor to:

track a buffer statistic;

provide a buffer status of said buffer statistic to a power threshold, wherein said buffer status represents a comparison of said buffer statistic and said statistic threshold; and

a power module to initiate a power conservation feature based on said buffer status.

29. – 30. (Canceled)

31. (Original) The system as in Claim 28, wherein said pending instructions include multimedia instructions.

32. (Original) The system as in Claim 31, wherein said multimedia instructions include display instructions.

33. (Canceled)

34. (Original) The system as in Claim 28, wherein said buffer statistic includes a fullness of said instruction buffer.

35. (Original) The system as in Claim 28, wherein said buffer statistic includes a number of pending instructions in said instruction buffer.

36. (Original) The system as in Claim 28, wherein said buffer statistic includes a rate of change in a number of pending instructions in said instruction buffer.

37. (Original) The system as in Claim 28, wherein said buffer statistic includes types of instructions in said instruction buffer.

38. (Previously Presented) A computer readable medium tangibly embodying a program of instructions to manipulate a data processor to:

identify a rate of change of a number of pending instructions stored in an instruction buffer; and

adjust a system characteristic based on the rate of change of the number of pending instructions, wherein a power consumption of the system is modified based on the system characteristic.

39. – 40. (Canceled)

41. (Previously Presented) The computer readable medium as in Claim 38, wherein the system characteristic includes a number of bits used to represent a multimedia data.

42. (Previously Presented) The computer readable medium as in Claim 38, wherein the system characteristic includes a clock speed used to process the instructions.

43. (Original) The computer readable medium as in Claim 38, wherein the system characteristic includes a supported power.

44. (Canceled)

45. (Previously Presented) The method as in Claim 1, wherein adjusting the system characteristic includes modifying a clock speed.

46. (Previously Presented) The method as in Claim 1, wherein the adjusting the system characteristic includes modifying a maximum power provided to the system.

47. (Canceled)

48. (Currently Amended) A method comprising:

identifying an operating characteristic of an instruction buffer, the operating characteristic comprising at least one of a buffer fullness[[,]] or a rate of change of a number of pending instructions stored in the instruction buffer; ~~or a type of instructions stored in the instruction buffer;~~ and

adjusting a system characteristic based on the operating characteristic, wherein the operating characteristic includes a type of instructions stored in the instruction buffer and wherein adjusting the system characteristic includes modifying a clock speed.

49. (Canceled)

50. (Canceled)

51. (Previously Presented) The system as in Claim 28, wherein the power conservation feature includes a modification of a clock speed.

52. (Previously Presented) The system as in Claim 28, wherein the power conservation feature includes a modification of a maximum power provided to the system.

53. (Currently Amended) The system as in Claim 28, wherein the power conservation feature includes a modification of a number of bits to represent multimedia data.